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(25) Filing Language: English (71) Applicant (for all designated States except US): AXALTO SA [FR/FR]; 50, avenue Jean Jaurès, BP 620, F-92120 Montrouge (FR).

(26) Publication Language: English (71) Applicant (for MC only): SCHLUMBERGER MALCO INC [US/US]; 9800 REISTERTOWN, OWING MILLS, MD 21117 (US).

(30) Priority Data: 03290688.5 18 March 2003 (18.03.2003) EP (71) Applicant (for all designated States except US): LY, Olivier, Thanh-Khiêt [FR/FR]; 89 avenue de la République, F-92100 BOULOGNE BILLANCOURT (FR).

(72) Inventors; and (75) Inventors/Applicants (for US only): AKKAR, Mehdi-Laurent [FR/FR]; 17 Rue Lafouge, F-94250 GENTILLY (FR). GOUBIN, Louis [FR/FR]; 3 Rue Brown Sevard, F-75015 PARIS (FR).

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(54) Title: METHOD TO SECURE AN ELECTRONIC ASSEMBLY EXECUTING ANY ALGORITHM AGAINST ATTACKS BY ERROR INTRODUCTION

(57) Abstract: The invention concerns an automatic method to secure an electronic calculation assembly against attacks by error introduction or by radiation. The following are used: 1) Static information generated by the automatic process; 2) A dynamic part of the memory of the electronic system allocated by the automatic process; 3) Beacons and check points to mark out the code, introduced by the automatic process; 4) Beacon functions storing information in the dynamic memory; 5) History verification functions using the static information and the dynamic memory to check that no errors have been introduced.